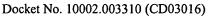
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TRANSMITTAL RORM			Application Number		10/807,909		<u> </u>
			Filing Date		March 24, 2004		
			First Named Inventor		Sanjay Rekhi		
			Art Unit		2818		
be used for all correspondence after initial filing)			Examiner Name		Dang, Phuc T		
Total Number of Pages in This			Attorney Docket N	lumber	10002.00	3310 (CD03016)	J
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Date		June 15, 2005 Reg. No.		40,909			
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Response To Office Action

50 pe 15, 2005

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Ye Application of:

Sanjay Rekhi et al.

Application No.:

10/807,909

Examiner:

Dang, Phuc T

Filing Date:

March 24, 2004

Art Unit:

2818

Title:

Protection of Integrated Circuit Gates During Metallization Processes

Honorable Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

INTRODUCTORY COMMENTS

Sir:

This paper is responsive to the office action mailed on March 29, 2005 (hereinafter the "last office action").